REMARKS

Summary of Office Action

The drawings stand objected to under 37 C.F.R. §1.83(a) for allegedly failing to show every feature of the invention specified in the claims.

Claims 1 and 3 stand rejected under §102(b) as allegedly being anticipated over Eto et al. (US 5,301,031).

Claims 1 and 3 stand rejected under §103(a) as allegedly being unpatentable over Yamaguchi (US 6,329,975) in view of Eto et al.

Claims 2 and 4-5 stand rejected under §103(a) as allegedly being unpatentable over Eto et al. in view of Sekido et al. (US 5,999,158).

Claims 2 and 4-5 stand rejected under §103(a) as allegedly being unpatentable over Yamaguchi, Eto et al. and further in view of Sekido et al.

Claim 6 stands objected to as being dependent upon a rejected base claim, but is indicated as being allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Summary of Amendment

None of the claims have been amended at this time. Claim 7 stands canceled without prejudice or disclaimer. Accordingly, claims 1-6 are pending in this application for further consideration.

Allowable Subject Matter

Applicant thanks the Examiner for indicating allowable subject matter in claim 6. Applicant believes that the other claims are also allowable for the reasons stated below. Therefore, claim 6 has not been rewritten at this time.

Drawing Objections

The drawings stand objected to under 37 C.F.R. §1.83(a) for allegedly failing to show every feature of the invention specified in the claims. In particular, the Office alleges that "a source shift clock reset unit" and "a reference clock generator" must be shown or the features canceled from the claims. Applicant submits that the replacement drawings filed June 20, 2006 shows all the claimed features. Moreover, the substitute specification filed June 20, 2006 describes all of the elements illustrated in the replacement drawings. Accordingly, Applicant submits that the drawings and specification are in compliance of 37 C.F.R. §1.83(a).

All Claims Comply With §102 and §103

Claims 1 and 3 stand rejected under §102(b) as allegedly being anticipated over Eto et al. Claims 1 and 3 stand rejected under §103(a) as allegedly being unpatentable over Yamaguchi in view of Eto et al. Claims 2 and 4-5 stand rejected under §103(a) as allegedly being unpatentable over Eto et al. in view of Sekido et al. Claims 2 and 4-5 stand rejected under §103(a) as allegedly being unpatentable over Yamaguchi, Eto et al. and further in view of Sekido et al. Applicant respectfully traverses for the following reasons.

Independent claims 1 and 3 recite, in part, "the source shift clock is reset at said enable initiation time in response to the reset signal." The Office alleges that Eto et al. teaches a source

shift clock (14 or CKT) reset by reset signal (STH). Applicant disagrees.

FIG. 1 of Eto et al. is reproduced and annotated below for convenience.
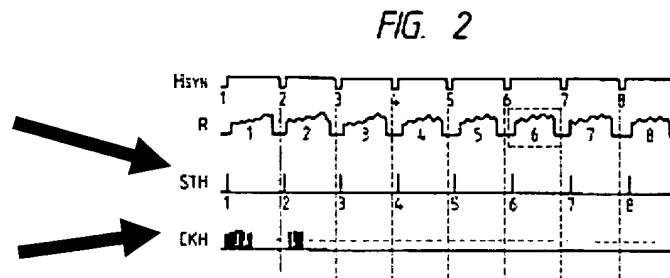

FIG. 1

"reset signal"
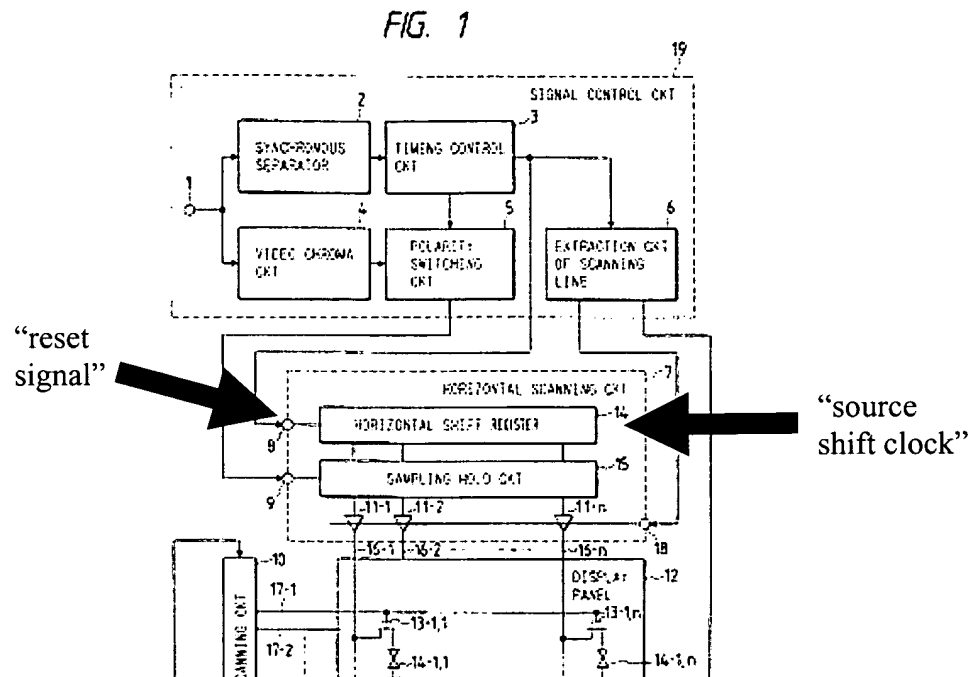
"source shift clock"

In particular, the Office alleges that circuits 2 and 3 is a "source shift clock reset unit" and circuit

14 or CKT is a "reference clock generator" for generating a source shift clock that is reset by

reset signal (STH). (OA: p. 3, §3.) To the contrary, the STH signal (node 8) is not a reset signal

and circuit 14 is not a source shift clock.

As explained in the previous response, circuit 14 is a horizontal shift register, not a

source shift clock. Furthermore, the STH signal (node 8) is not a reset signal that resets the

source shift clock. Rather, the STH signal is a sampling starting pulse that allows the horizontal

shift register 14 to begin sampling the video signal present at node 9. (Eto: col. 4, ll. 6-10.) In

the Office Action, it is alleged that FIG. 2 of Eto et al. teaches that the STH signal resets the

source shift clock CKH. Again, Applicant disagrees.

FIG. 2 of Eto et al. is reproduced and annotated below for convenience.

FIG. 2

As disclosed, Eto et al. teaches that the starting pulse STH (FIG. 1: node 8) is generated when a video signal is present (FIG. 1: node 9). The CKH represents the sampling clock generated by a horizontal shift register shift clock (not shown). (Eto: col. 4, ll. 8-9.) The horizontal shift register 14 *does not* generate the sampling clock signal CKH as alleged in the Office Action. Morover, as shown in FIG. 2 of Eto et al. reproduced above, the sampling clock signal CKH is produced continuously by a horizontal shift register shift clock (not shown). There is no teaching that CKH is reset. Hence, Eto et al. fails to teach or even suggest that the horizontal shift register shift clock (not shown), which generates the sampling clock signal CKH, is reset by the starting pulse signal STH. Accordingly, Eto et al. fails to anticipate independent claims 1 and 3 for at least the reasons stated above.

In the alternative, the Office alleges that Yamaguchi teaches resetting a source shift clock 8 using a reset signal based on a data enable signal as shown in FIG. 4 of Yamaguchi. (OA: p. 4, §5.) Applicant disagrees.

Portions of FIG. 3 and 4 of Yamaguchi are reproduced and annotated below for convenience.
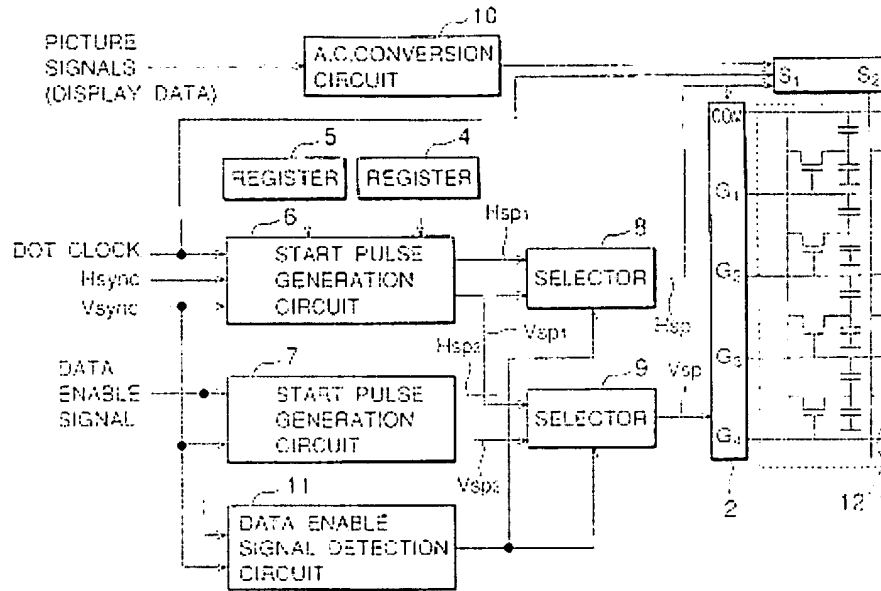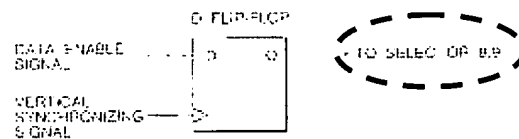
FIG.3

FIG.4

Firstly, the selector circuit 8 is *not* a source shift clock. As described in column 4, lines 64-66 and column 5, lines 14-16 of Yamaguchi, selector 8 selects between either H_{sp1} or H_{sp2} as the *starting pulse* for the source driver 3. A starting pulse is not a source shift clock.

Secondly, the signal generated by the data enable signal section circuit 11 (FIG. 4) generates a starting pulse select signal to be sent to selector 8, 9. The signal generated in FIG. 4 of Yamaguchi is *not* a reset signal that resets a source shift clock as alleged in the Office Action. Moreover, Eto et al. does not cure these deficiencies as discussed above. Accordingly,

Yamaguchi and Eto et al., whether taken individually or in combination, fail to teach at least "the source shift clock is reset at said enable initiation time in response to the reset signal" as recited in independent claims 1 and 3. Hence, Applicant respectfully request that the rejections to independent claims 1 and 3 be withdrawn.

Claims 2, 4, and 5 depend from and incorporates all features of corresponding one of independent claims 1 and 3 including the above features missing from Yamaguchi and Eto et al. Sekido et al. does not cure the deficiencies of Eto et al. and Yamaguchi. Therefore, Yamaguchi, Eto et al., and Sekido et al., whether taken individually or in combination, fail to teach at least the features discussed above. Hence, Applicant respectfully request that the rejections to claims 2, 4, and 5 be withdrawn.

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CONCLUSION

In view of the foregoing, allowance of all pending claims is earnestly solicited. Should

the Examiner feel that there are any issues outstanding after consideration of the response, the

Examiner is invited to contact the Applicant's undersigned representative to expedite

prosecution.

If there are any other fees due in connection with the filing of this response, please

charge the fees to our Deposit Account No. 50-0310. If a fee is required for an extension of time

under 37 C.F.R. 1.136 not accounted for above, such an extension is requested and the fee should

also be charged to our Deposit Account.

Respectfully submitted,

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